

## ABSTRACT OF THE DISCLOSURE

A method for autocalibrating a plurality of phase-  
delayed clock signal edges within a reference clock pe-  
5 riod includes measuring delay spacing between the plu-  
rality of clock signal edges, calculating programmed de-  
lay spacing, calculating ideal signal edges from the pro-  
grammed delay spacing and adjusting the clock signal  
edges to match the respective ideal signal edges. A plu-  
10 rality of calibrated clock signal edges is produced that  
are selectively available to a user.